



DESIGN SYSTEM

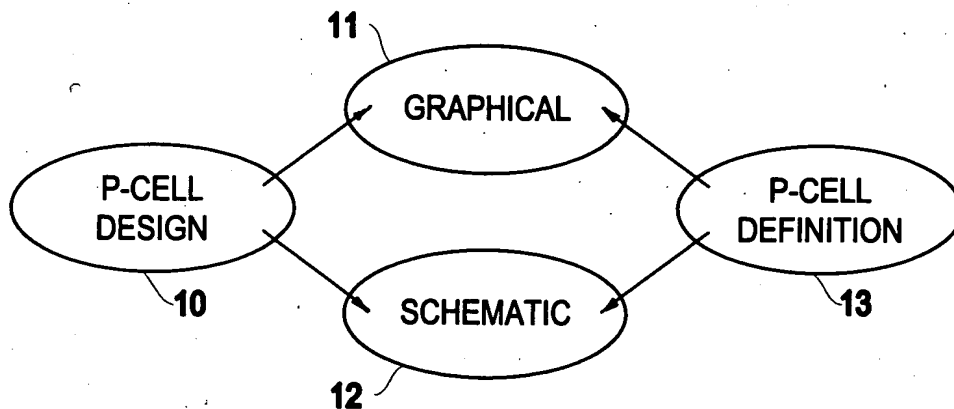


FIG.1

DESIGN SYSTEM

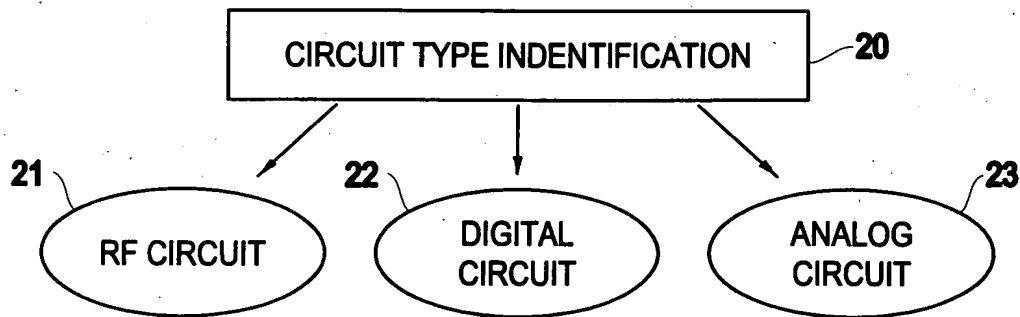


FIG.2

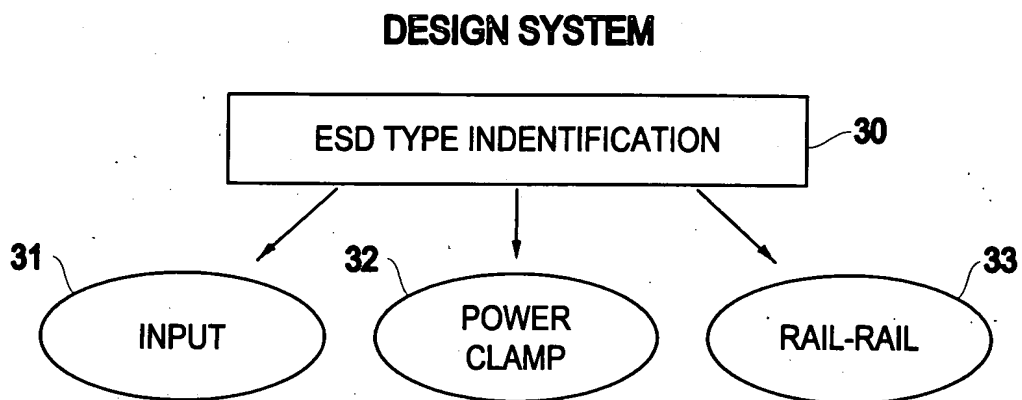


FIG.3

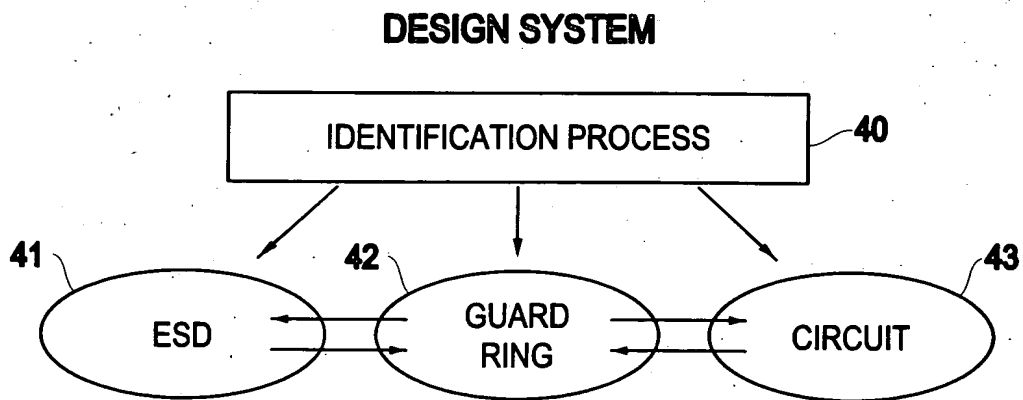


FIG.4

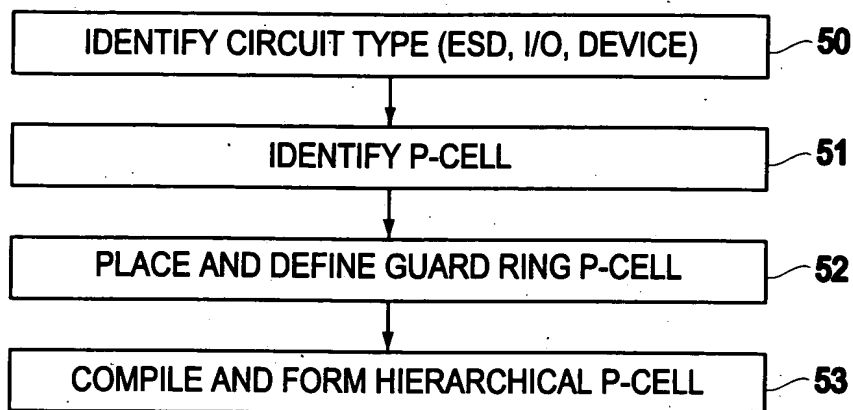
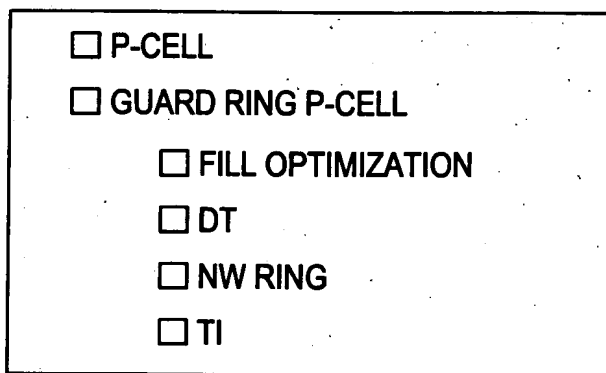
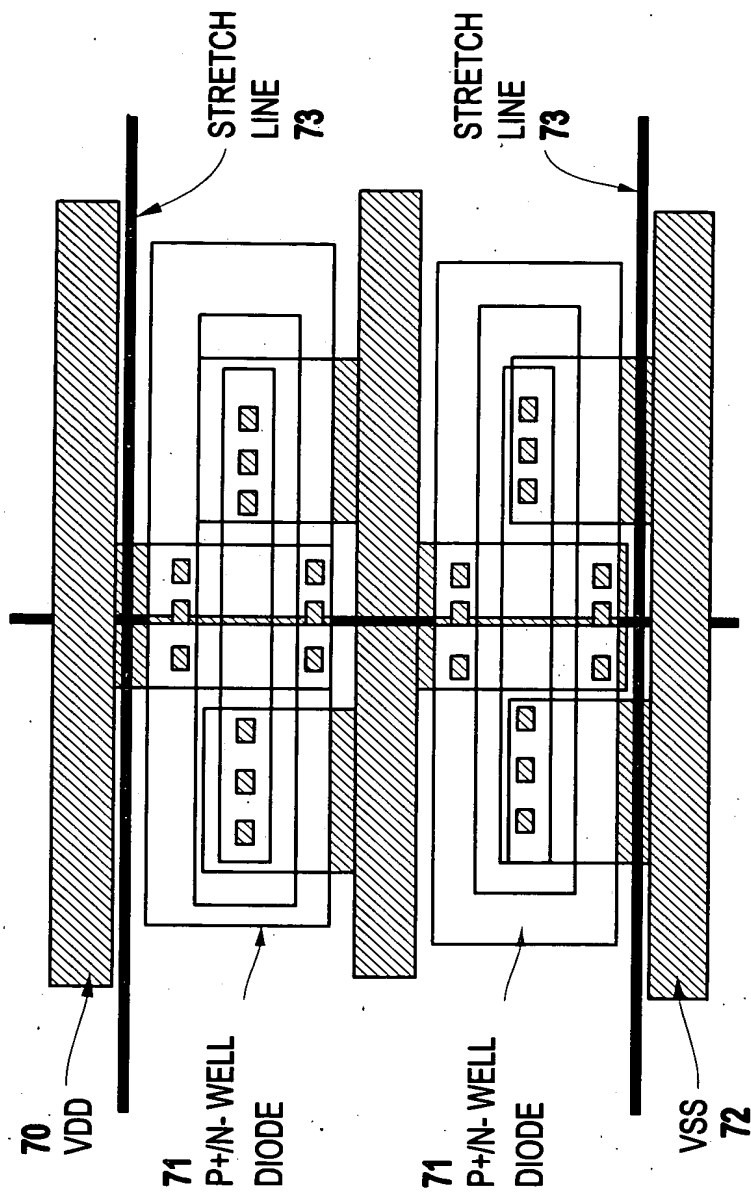


FIG.5



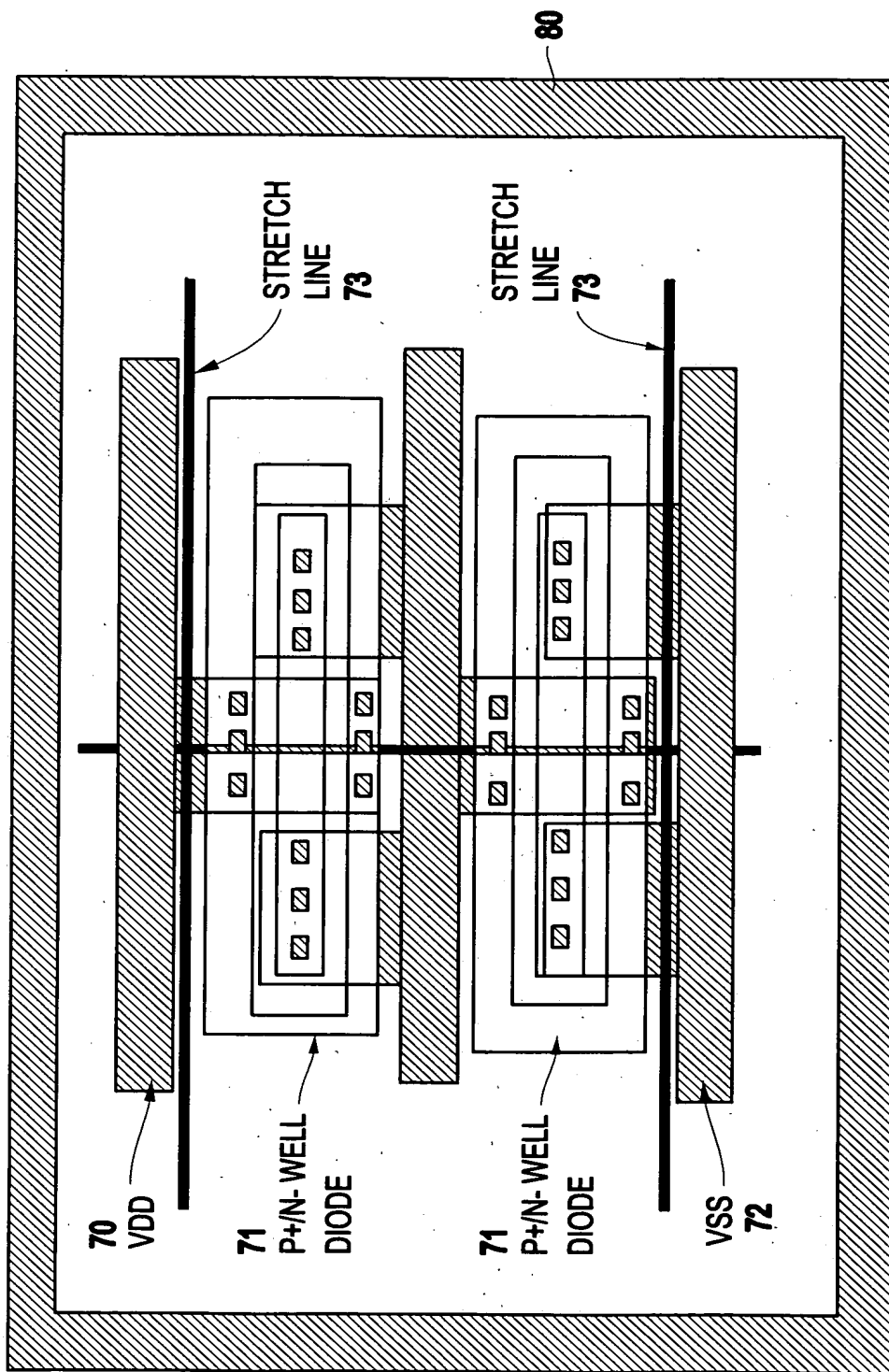
P-CELL/GUARD RING DESIGN P-CELL GRAPHIC UNIT INTERFACE

FIG.6



GRAPHICAL ESD HIERARCHICAL P-CELL

FIG.7



ESD HIERARCHICAL P-CELL WITH GUARD RING

FIG.8

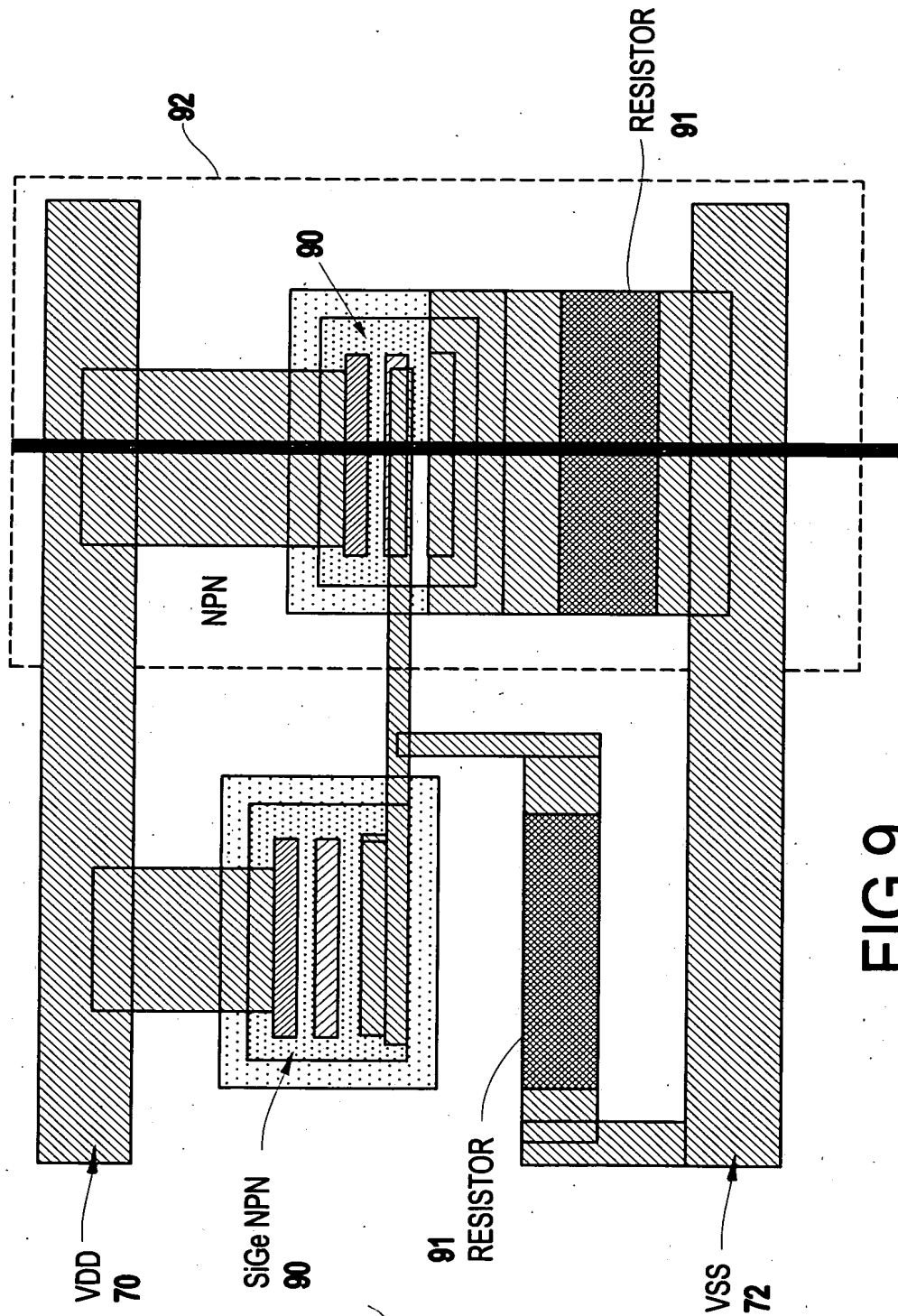


FIG. 9

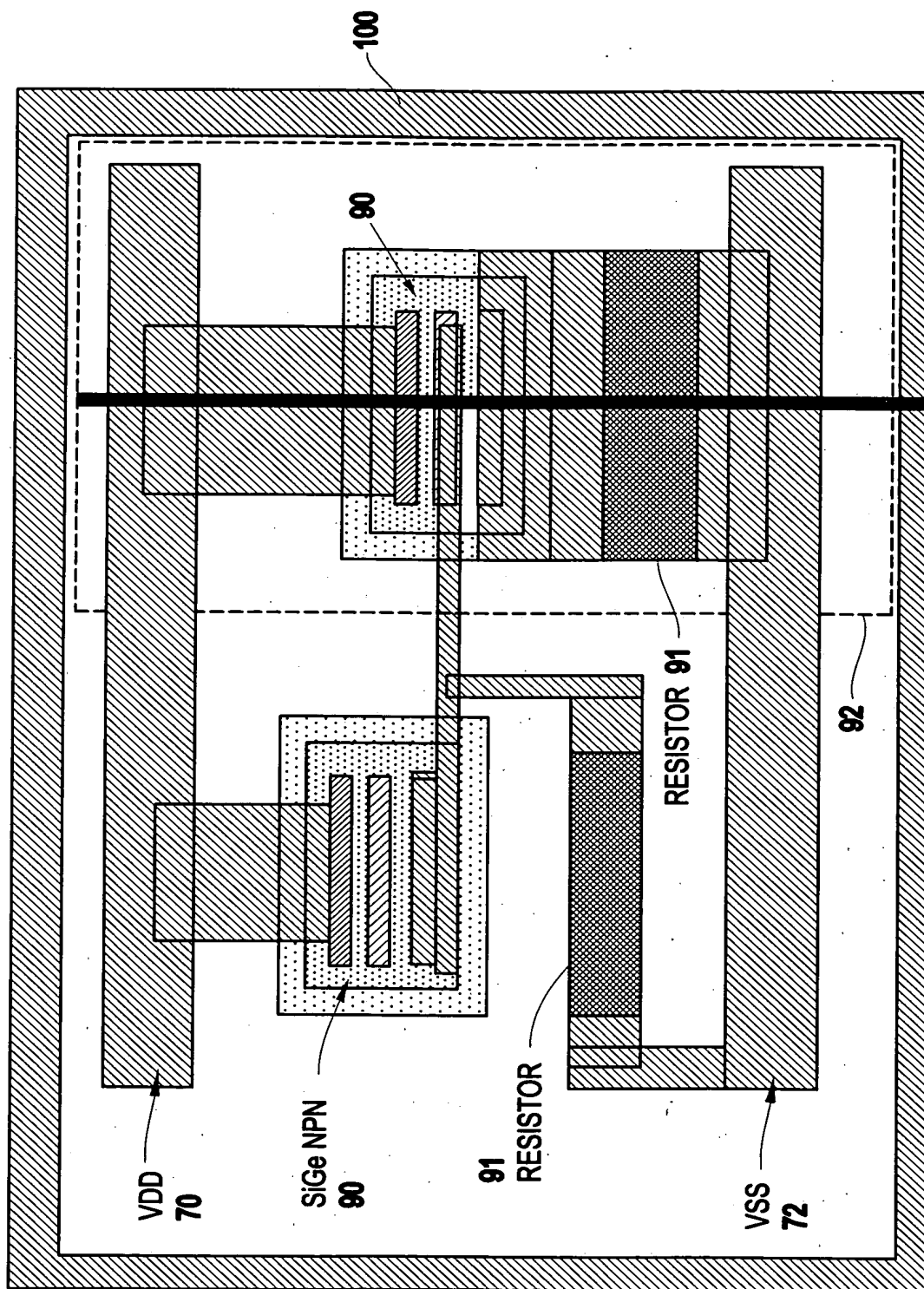


FIG.10

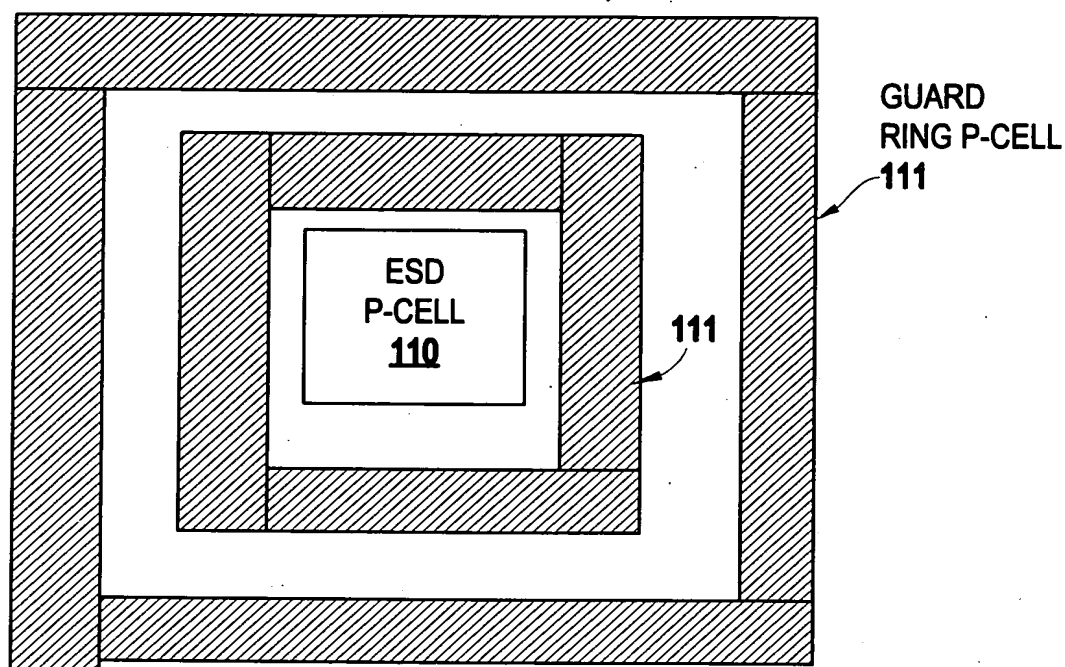


FIG. 11

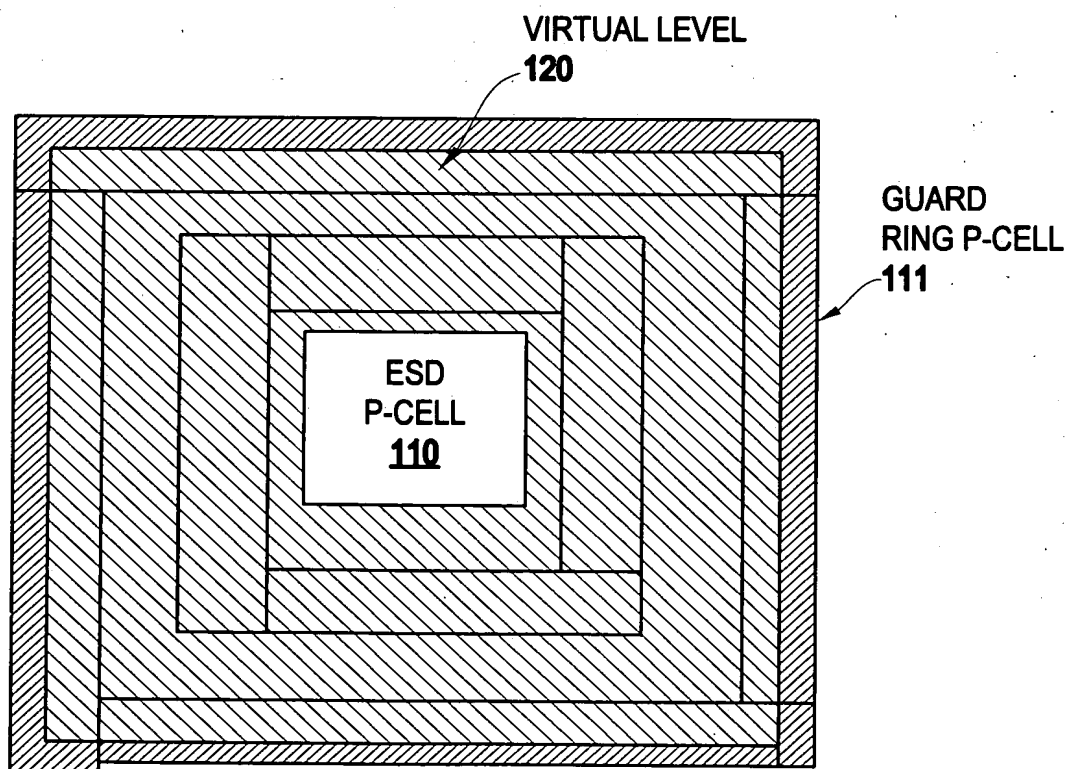


FIG. 12

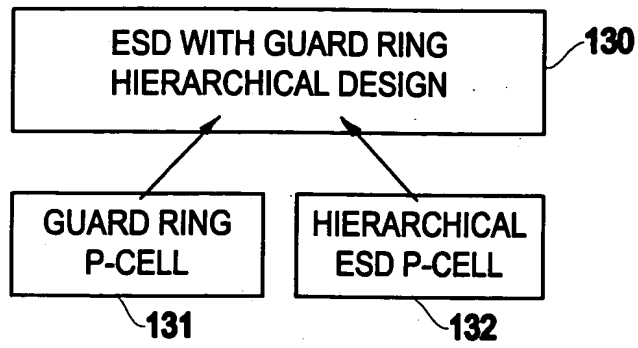


FIG.13

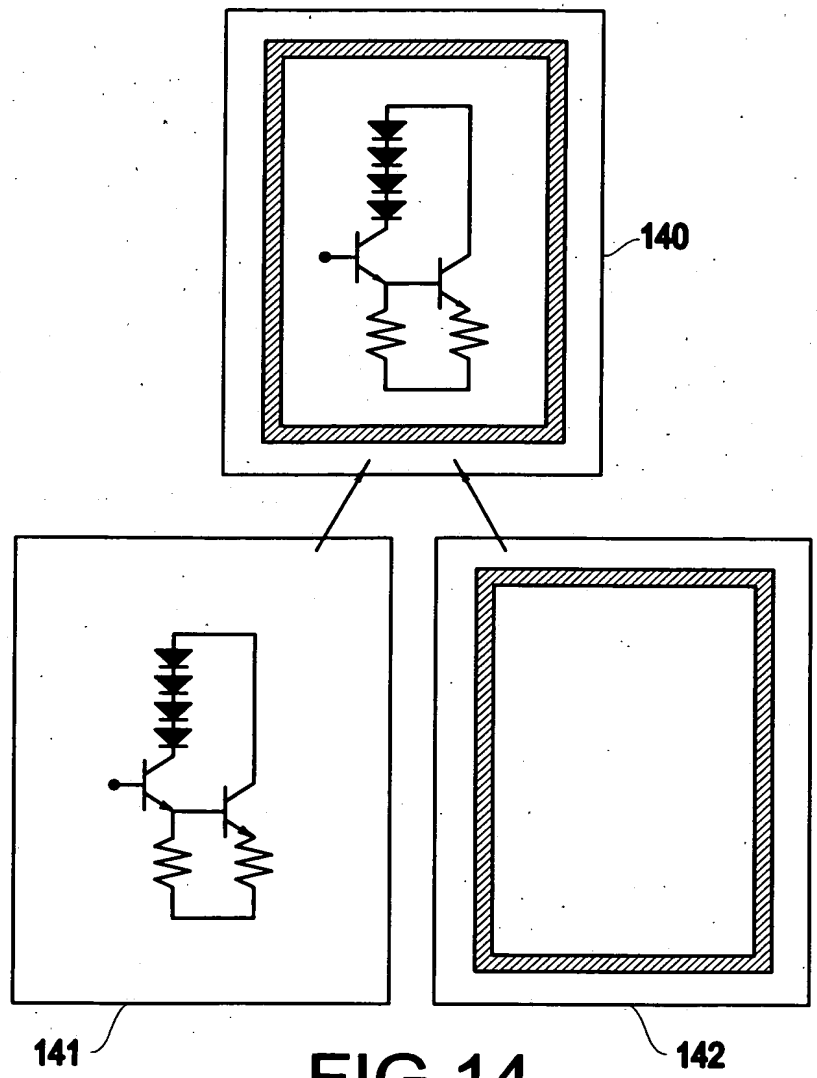


FIG.14